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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/680,798	10/06/2000	Alain Benayoun	FR9-1999-0035 US1	2906	
42640	7590 10/19/2005		EXAMINER		
	YUDELL LLP	BURGESS, BARBARA N			
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AUSTIN, T	X 78759	2157			

DATE MAILED: 10/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			Application No.	Applicant(s				
Office Action Summary			09/680,798	BENAYOUN	BENAYOUN ET AL.			
			Examiner	Art Unit				
			Barbara N. Burgess	2157				
Period fo	The MAILING DATE of this commu or Reply	nication appe	ars on the cover sheet	with the corresponden	ce address			
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD IN CHEVER IS LONGER, FROM THE IN Insions of time may be available under the provision SIX (6) MONTHS from the mailing date of this comperiod for reply is specified above, the maximum is to reply within the set or extended period for reply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	MAILING DA' is of 37 CFR 1.136 imunication. statutory period will ly will, by statute, c	TE OF THIS COMMUN (a). In no event, however, may I apply and will expire SIX (6) Mause the application to become	NICATION. a reply be timely filed ONTHS from the mailing date of ABANDONED (35 U.S.C. § 13	f this communication.			
Status								
1)⊠	Responsive to communication(s) fil	ed on <i>04 Au</i> d	aust 2005.					
2a)□	,		action is non-final.					
3)□								
·	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims							
4)🖂	☑ Claim(s) <u>1 and 7-27</u> is/are pending in the application.							
-	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)□	☐ Claim(s) is/are allowed.							
6)⊠								
7)								
8)□	Claim(s) are subject to restri	iction and/or	election requirement.					
Applicati	on Papers							
9)□	The specification is objected to by the	ne Examiner.						
-				o by the Examiner.				
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including		• • • • • • • • • • • • • • • • • • • •		• •			
11)	The oath or declaration is objected t	-	·					
Priority ι	ınder 35 U.S.C. § 119							
•	Acknowledgment is made of a claim	n for foreign p	niority under 35 U.S.C	. § 119(a)-(d) or (f).				
a)	☐ All b)☐ Some * c)☐ None of:							
	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority							
	3. Copies of the certified copies			en received in this Nati	onal Stage			
	application from the Internation							
* 8	See the attached detailed Office action	on for a list of	rtne certified copies no	ot received.				
Attachmen	` '		4) T 1	v Summary (PTO-413)	•			
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (I	PTO-948)		o(s)/Mail Date				
3) 🔲 Inforr	nation Disclosure Statement(s) (PTO-1449 or r No(s)/Mail Date	•	5) Notice o	f Informal Patent Application	ı (PTO-152)			

Application/Control Number: 09/680,798 Page 2

Art Unit: 2157

DETAILED ACTION

This Office Action is in response to amendment filed August 4, 2005. Claims 1, 7-27 are presented for further examination.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1, 7-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bucher et al. (hereinafter "Bucher", US Patent No. 6,393,587 B2) in view of Jou (US Patent No. 6,389,000 B1).

As per claims 1, 15, 22, Bucher discloses a communication device, computer program product, and method for transferring data between two devices coupled to a network, said communication device comprising:

- A dual-port memory for storing data (column 6, lines 42-55);
- A high-speed interface for transmitting, said data between a first device and said dual port memory, wherein said high-speed interface communicates data at an initial rate (column 7, lines 35-55);

Application/Control Number: 09/680,798

Art Unit: 2157

A plurality of low-speed interfaces, each connected to a respective one of a
plurality of links to said second device, for transmitting data from said dual-port
memory to said respective one of said plurality of links at one of a plurality of
data rates, wherein at least two of said plurality of data rates are unequal and are
fractions of said initial rate, all said fractions being capable of reduction to a
common denominator and at least one of said fractions being irreducible (column
9, lines 11-35).

Bucher does not explicitly disclose:

Controller for controlling said memory and said interfaces and for monitoring a
data rate of said data between said memory and said plurality of links, wherein
said controller includes means for cyclically distributing data to be communicated
from said memory to said second device among said low-peed interfaces, such
that each of said plurality of low speed interfaces receives a number of
consecutive units of said data equal to the numerator of its associated fraction.

However, in an analogous art, Jou discloses a control processor selecting a format that is capable to transmitting data at a selected rate (column 5, lines 35-40, column 8, lines 11-55).

Therefore, one of ordinary skill in the art at the time the invention was made would have found it obvious to incorporate or implement Jou's controller in Bucher's device in order to transmit data a particular rate.

As per claims 7, 16, 23, Bucher discloses the communication device of Claim 1, wherein, at least one of the set of said high-speed interface and said plurality of low speed interfaces comprises means for establishing a connection with a modem (column 9, lines 11-20).

As per claims 10, 17, 24, Bucher discloses the communication device of Claim 1 wherein said hi-speed interface further comprises means for receiving said data at an said initial rate, wherein said initial rate is equal to a sum of said plurality of data rates (column 8, lines 30-46).

As per claims 11, 18, 25, Bucher discloses the communication device of Claim 10, wherein at least two of said data rates are equal (column 8, lines 60-65).

As per claims 12, 19, 26, B ucher discloses the communication device of Claim 1, wherein said initial rate equals a sum of said plurality of data rates (column 7, lines 31-55).

As per claims 13, 20, 27, Bucher does not explicitly discloses the communication device of Claim 1 said controller further comprises means for reporting said data rates. However, in an analogous art, Jou discloses a control processor selecting a format that is capable to transmitting data at a selected rate (column 5, lines 35-40, column 8, lines 11-55).

Application/Control Number: 09/680,798

Art Unit: 2157

Therefore, one of ordinary skill in the art at the time the invention was made would have found it obvious to incorporate or implement Jou's controller in Bucher's device in order to transmit data a particular rate.

As per claims 14, 21, Bucher discloses the communication device of Claim 1, wherein said high speed interface further comprises means for transmitting said data at said initial rate wherein said initial rate is equal to a sum of said plurality of data rates and at least two of said data rates are unequal (column 7, lines 60-67).

Response to Arguments

3. Applicant's arguments have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Barbara N Burgess whose telephone number is (571) 272-3996. The examiner can normally be reached on M-F (8:00am-4:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ario Etienne can be reached on (571) 272-4001. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2157

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Barbara N Burgess Examiner Art Unit 2157

October 17, 2005

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100